

**REVISIONS**

REV	DESCRIPTION	DATE	APPROVED
A	VERSION 3.0.15	1/30/17	RH

Full Documentation & Code : "\\fetco-file\Software\Firmware\_Releases\1051.00020.00\v3.0.15\_Release"

**Significant Changes:**

1. Modifications made to QT Timer Setups improves reliability by preventing "ERROR 1" indications
2. Welcome Screen - displays copyright 2017
3. Power On screen - Briefly display (3 seconds) App and BL versions
4. Temperature Probe Operation/Removal/Testing
  - Programming mode can be entered with the probe removed or shorted
  - probe can be removed while in Programming mode without interruption
  - Input Summary Screen reflects status of probe - temperature/open/short
5. LLC-L is not recognized in the program logic.
6. Input Summary Screen change - minor changes to screen
  - removal of LLC-L
  - temperature probe status indicates temperature, open, or short
7. Error Code 100 and 101 Timeout times have changed - see Error\_100\_101\_Times.txt for details

**Firmware Programming:**

1. SD-Card Installation: CBS2130.frm - Application  
(MD5 Checksum: 6eff2668954050f357962e2feae881ca)
2. External Programmer: cbs2030\_3015\_w\_bl.hex - intel hex file of Bootloader and CBS Application  
(MD5 Checksum: 7f8081c05381ea3f3d8d87491c6a8f5b)
3. Programmed Checksum: [0x08000000:0x08078D00] Memory Checksum: 0x04165744  
- this is the result of programming the PCB using the ST-Link V2 Programmer

CODE & DOCUMENTATION DIRECTORY:  
\\fetco-file\Software\Firmware\_Releases\1051.00020.00



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# FIRMWARE, CBS-2100 XTS

CREATED BY RICHARD HOPPEL	DRAWN BY RICHARD HOPPEL	SIZE <b>A</b>	DATE 1/30/2017	DWG NO <b>1050.00019.FW</b>	REV <b>A</b>
FILENAME 1050.00019.FW.VSDX		SCALE 1 : 1	SHEET		1 OF 1